

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Steven J. Goldman

Application No.: New

Filed: Herewith

For: METHOD OF TRANSFERRING
SIGNALS BETWEEN A TTL
MICROCONTROLLER AND A RS232
DEVICE

Group Art Unit: Unknown

Examiner: Unknown

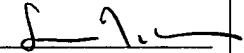
**INFORMATION DISCLOSURE
STATEMENT**

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San Francisco, CA 94105
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Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING
I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 27, 2004.

STALLMAN & POLLOCK LLP

Dated: 02/27/04 By: 

~~Georgia K. Smith~~ **LANA T. BRENNER**

Sir:

Applicant(s) submit(s) herewith patents, publications or other information [attached hereto and listed on the attached Form PTO-1449 (modified)] of which he is aware, which he believe(s) may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

This Information Disclosure Statement:

- (a) accompanies the new patent application submitted herewith. 37 CFR § 1.97(a).
- (b) is filed within three months after the filing date of the application or within three months after the date of entry of the national stage of a PCT application as set forth in 37 CFR § 1.491.
- (c) as far as is known to the undersigned, is filed before the mailing date of a first Office Action on the merits, or before a first office action after filing a Request for Continued Examination under §1.114.

(d) is filed after the first office action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned, prior to the mailing date of either a final rejection or a notice of allowance, whichever occurs first, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below.

(e) is filed after the mailing date of either a final rejection or a notice of allowance, whichever occurred first, and the Issue Fee has not been paid, and is accompanied by the fee (\$130) set forth in 37 CFR § 1.17(i)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.

[If either of boxes (d) or (e) is checked above, the following "certification" under 37 CFR § 1.97(e) may need to be completed.] The undersigned certifies that:

(f) Each item of information contained in the information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

(g) No item of information contained in this information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this information disclosure statement.

A list of the patent(s) or publication(s) is set forth on the attached Form PTO-1449 (Modified).

A copy of the items on PTO-1449 (Modified) is supplied herewith, except as noted below.

Those patent(s) or publication(s) which are marked with an asterisk (*) in the attached form PTO-1449 (Modified) are not supplied because they are (a) either U.S. Patents and this an application filed after June 30, 2003, or (b) were previously cited by or submitted to the Office in a prior application no. 09/483,628, filed January 14, 2000 and relied upon in this application for an earlier filing date under 35 U.S.C. § 120.

A concise explanation of relevance of the items listed on form PTO-1449 (Modified) is:

- (k) not given
- (l) given for each listed item
- (m) given for only non-English language listed item(s) [Required]
- (n) is in the form of an English language copy of a Search Report from a foreign patent office, issued in a counterpart application, which refers to the relevant portions of the references [copy attached].

The Examiner is reminded that a "concise explanation of the relevance" of the submitted items "may be nothing more than identification of the particular figure or paragraph of the patent or publication which has some relation to the claimed invention," MPEP § 609.

While the information and references disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the listed references.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: 2-27-2004

By: Michael J. Pollock
Michael J. Pollock
Reg. No. 29,098

Attorneys for Applicant(s)

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>		Docket Number (Optional) NSC1-G0810 [P04233 D1]		Application Number New	
		Applicant(s) Charles Dark et al..			
		Filing Date Herewith		Group Art Unit Unknown	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	AA	* 4,130,883	12/19/78	Hazelton	364	900	10/14/75
	AB	* 5,951,660	09/14/99	Van Wonteghem	710	103	12/10/97
	AC	* 5,506,991	04/09/96	Curry et al.	395	800	12/19/90
	AD	* 3,961,326	06/01/76	Craven	340	347	09/12/74
	AE	* 4,725,836	02/16/88	Guidos	340	825.590	01/27/86
	AF	* 4,589,106	05/13/86	Prather et al.	370	58	08/20/84
	AG	* 4,764,939	08/16/88	Rogers	375	37	08/19/87
	AH	* 6,502,143	12/31/02	Bass et al.	710	1	12/31/99
	AI	* 5,956,523	09/21/99	Chen	395	882	08/09/96
	AJ	* 4,497,041	01/29/85	Braun	364	900	09/09/82
	AK	* 5,812,879	09/22/98	Moro	395	882	04/19/93
	AL	* 5,844,829	12/01/98	Freidin et al.	364	716.03	12/04/97
	AM	* 5,901,221	05/04/99	Eriksson et al.	379	399	11/08/95
	AN	* 5,596,637	01/21/97	Pasetti et al.	379	399	05/02/94
	AO	* 5,701,103	12/23/97	Fujii	330	290	07/10/96
	AP	* 4,491,804	01/01/85	Main et al.	330	265	11/18/82
	AR	* 4,489,283	12/18/84	Ishizaki	330	268	01/28/82
	AS	* 4,356,452	10/26/82	Iwamatsu	330	265	08/26/80
	AT	* 4,833,423	05/23/89	Molloy	330	265	07/20/87
	AU	* 4,509,113	04/02/85	Heath	364	200	02/02/82
	AV	* 5,432,698	07/11/95	Fujita	364	413.02	05/31/94
	AW	* 4,695,955	09/22/87	Faisandier	364	413	12/26/84

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

AX	* Ossman, M., "Single µC Pin Makes Half-Duplex RS-232C", Electronic Design News, pp. 118, August 5, 1999
AY	* "COP8SA Family 8-Bit CMOS ROM Based and One-Time Programmable (OTP) Microcontroller with 1K to 4K Memory, Power on Reset, and Very Small Packaging", National Semiconductor Corporation, Preliminary July 1999 (DS012838)
AZ	* "PC16550D Universal Asynchronous Receiver/Transmitter with FIFOs", National Semiconductor Corporation, TL/C/8652, June 1995
BA	* "DSV14196 +3.3V Supply EIA/TIA-232 5 Driver x 3 Receiver", National Semiconductor Corporation, DS100853, April 1999
BB	* Campbell G.W., "Low Power RS-232C Driver and Receiver in CMOS", National Semiconductor Corporation, AN008681, Application Note 438, July 1986
BC	* Goldie J., "Summary of Well Known Interface Standards", National Semiconductor Corporation, AN005855, Application Note 216, July 1998
BD	* Horowitz, P. et al., excerpts from "The Art of Electronics", Second Edition, Digital Signals and Long Wires, pp. 603; Digital Meets Analog, pp. 604; Low-Power Design, pp. 984; Self-Explanatory Circuits pp. 985, 1980, 1989
BE	* John F. Wakerly, Digital Design Principles & Practices, 1990, Prentice Hall, 3rd edition, pp. 157-158
BF	* ARC Electronic, RS232 Interface, 1999, http://www.arcelect.com/rs232.htm
BG	* ELM Electronics, ELM 624-Control L to RS232 Interpreter, 1999, http://www.elmelectronics.com

	BH	* ELM Electronics, ELM621-Serial Three Bit Port, 1999, http://www.elmelectronics.com
	BI	* TechEncyclopedia, PMOS definition, http://www.techweb.com/encyclopedia/defineterm?term=pmos

Examiner	Date Considered
Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	